

THE AMENDMENTS

In the Specification:

Please amend the section beginning on page 6 at line 2, entitled "Related Applications," as follows:

--RELATED APPLICATIONS

The following applications are commonly owned by the assignee hereof, are being filed on even date herewith, and are each incorporated by reference herein as though set forth in full:

Howrey Dkt. No. <u>U.S. Patent Application</u> <u>Serial No.</u>	Extreme Dkt. No.	Title
02453.0025.NPUS00 <u>10/814,725</u>	P111	PACKET PROCESSING SYSTEM ARCHITECTURE AND METHOD
02453.0025.NPUS01 <u>10/814,552</u>	P153	PACKET PROCESSING SYSTEM ARCHITECTURE AND METHOD
02453.0026.NPUS00 <u>10/814,556</u>	P122	PACKET DATA MODIFICATION PROCESSOR
02453.0027.NPUS00 <u>10/814,728</u>	P124	SYSTEM AND METHOD FOR PACKET PROCESSOR STATUS MONITORING
02453.0028.NPUS00 <u>10/814,545</u>	P126	METHOD AND SYSTEM FOR INCREMENTALLY UPDATING A CHECKSUM IN A NETWORK DATA PACKET

02453.0029.NPUS00 <u>10/814.729</u>	P127	SYSTEM AND METHOD FOR EGRESS PACKET MARKING
02453.0030.NPUS00 <u>10/813,731</u>	P128	SYSTEM AND METHOD FOR ASSEMBLING A DATA PACKET
02453.0032.NPUS00 <u>10/814,727</u>	P125	PACKET DATA MODIFICATION PROCESSOR COMMAND INSTRUCTION SET—

Please amend the paragraph beginning at page 28, line 10, as follows:

Please replace the abstract with the following paragraph:

--A processor readable medium storing a data structure for supporting one or more packet modification operations and methods of using the processor readable medium for packet modification are provided. The data structure has a pointer to a sequence of one or more commands stored in a first memory area and implementing one or more packet modification operations. The data structure also has a pointer to a burst of one or more data or mask items stored in a second memory area for use by the one or more commands.—

In the Drawings:

Please replace Figures 19-22 with the Replacement Sheets included herewith.